

10068898\_CLS  
Most Frequently Occurring Classifications of Patents Returned  
From A Search of 10068898 on June 15, 2004

Original Classifications

3 365/233  
2 326/16  
2 326/39  
2 327/202  
2 327/218  
2 365/189.05  
2 365/219  
2 716/6

Cross-Reference Classifications

4 327/211  
4 365/221  
3 326/41  
3 327/210  
3 365/194  
3 365/230.05  
3 365/233  
2 326/38  
2 326/93  
2 326/95  
2 326/98  
2 327/203  
2 327/212  
2 327/218  
2 365/189.07  
2 365/226  
2 365/230.06  
2 365/236  
2 365/239

Combined Classifications

6 365/233  
5 327/211  
4 327/218  
4 365/221  
3 326/39  
3 326/41  
3 327/210  
3 365/194  
3 365/230.05  
2 326/16  
2 326/38  
2 326/93

10068898\_CLS

2 326/95  
2 326/98  
2 327/165  
2 327/200  
2 327/202  
2 327/203  
2 327/212  
2 327/291  
2 365/189.01  
2 365/189.05  
2 365/189.07  
2 365/207  
2 365/219  
2 365/220  
2 365/226  
2 365/230.06  
2 365/236  
2 365/239  
2 377/44  
2 710/53  
2 711/157  
2 714/727  
2 716/6

10068898\_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 10068898 on June 15, 2004

- 6 365/233 (3 OR, 3 XR)
  - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
  - 365/230.01 ADDRESSING
  - 365/233 .Sync/clocking
- 5 327/211 (1 OR, 4 XR)
  - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
  - 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
  - 327/185 .Particular stable state circuit (e.g., tristable, etc.)
  - 327/199 ..Circuit having only two stable states (i.e., bistable)
  - 327/208 ...Including field-effect transistor
  - 327/210 ....CMOS
  - 327/211 .....With clock input
- 4 327/218 (2 OR, 2 XR)
  - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
  - 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
  - 327/185 .Particular stable state circuit (e.g., tristable, etc.)
  - 327/199 ..Circuit having only two stable states (i.e., bistable)
  - 327/215 ...Having at least two cross-coupling paths
  - 327/218 ....D type input
- 4 365/221 (0 OR, 4 XR)
  - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
  - 365/189.01 READ/WRITE CIRCUIT
  - 365/221 .Serial read/write
- 3 326/39 (2 OR, 1 XR)
  - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
  - 326/37 MULTIFUNCTIONAL OR PROGRAMMABLE (E.G., UNIVERSAL, ETC.)
  - 326/39 .Array (e.g., PLA, PAL, PLD, etc.)
- 3 326/41 (0 OR, 3 XR)
  - Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
  - 326/37 MULTIFUNCTIONAL OR PROGRAMMABLE (E.G., UNIVERSAL, ETC.)

10068898\_CLSTITLES

326/39            .Array (e.g., PLA, PAL, PLD, etc.)  
326/41            ..Significant integrated structure, layout, or  
                  layout interconnections

3   327/210           (0 OR, 3 XR)  
      Class   327 :   MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR  
                      DEVICES, CIRCUITS, AND SYSTEMS  
327/100            SIGNAL CONVERTING, SHAPING, OR GENERATING  
327/185            .Particular stable state circuit (e.g.,  
                      tristable, etc.)  
327/199            ..Circuit having only two stable states (i.e.,  
                      bistable)  
327/208            ...Including field-effect transistor  
327/210            ....CMOS

3   365/194           (0 OR, 3 XR)  
      Class   365 :   STATIC INFORMATION STORAGE AND RETRIEVAL  
365/189.01        READ/WRITE CIRCUIT  
365/191            .Signals  
365/194            ..Delay

3   365/230.05       (0 OR, 3 XR)  
      Class   365 :   STATIC INFORMATION STORAGE AND RETRIEVAL  
365/230.01        ADDRESSING  
365/230.05        ..Multiple port access

2   326/16           (2 OR, 0 XR)  
      Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY  
326/16            WITH TEST FACILITATING FEATURE

2   326/38           (0 OR, 2 XR)  
      Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY  
326/37            MULTIFUNCTIONAL OR PROGRAMMABLE (E.G.,  
                      UNIVERSAL, ETC.)  
326/38            ..Having details of setting or programming of  
                      interconnections or logic functions

2   326/93           (0 OR, 2 XR)  
      Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY  
326/93            CLOCKING OR SYNCHRONIZING OF LOGIC STAGES OR  
                      GATES

2   326/95           (0 OR, 2 XR)  
      Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY  
326/93            CLOCKING OR SYNCHRONIZING OF LOGIC STAGES OR  
                      GATES  
326/95            ..Field-effect transistor

## 10068898\_CLSTITLES

- 2 326/98 (0 OR, 2 XR)  
     Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY  
     326/93     CLOCKING OR SYNCHRONIZING OF LOGIC STAGES OR  
                   GATES  
     326/95     .Field-effect transistor  
     326/98     ..MOSFET
- 2 327/165 (1 OR, 1 XR)  
     Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR  
                   DEVICES, CIRCUITS, AND SYSTEMS  
     327/100     SIGNAL CONVERTING, SHAPING, OR GENERATING  
     327/165     .Regenerating or restoring rectangular (e.g.,  
                   clock, etc.) or pulse waveform
- 2 327/200 (1 OR, 1 XR)  
     Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR  
                   DEVICES, CIRCUITS, AND SYSTEMS  
     327/100     SIGNAL CONVERTING, SHAPING, OR GENERATING  
     327/185     .Particular stable state circuit (e.g.,  
                   tristable, etc.)  
     327/199     ..Circuit having only two stable states (i.e.,  
                   bistable)  
     327/200     ...Dynamic bistable
- 2 327/202 (2 OR, 0 XR)  
     Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR  
                   DEVICES, CIRCUITS, AND SYSTEMS  
     327/100     SIGNAL CONVERTING, SHAPING, OR GENERATING  
     327/185     .Particular stable state circuit (e.g.,  
                   tristable, etc.)  
     327/199     ..Circuit having only two stable states (i.e.,  
                   bistable)  
     327/202     ...Master-slave bistable latch
- 2 327/203 (0 OR, 2 XR)  
     Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR  
                   DEVICES, CIRCUITS, AND SYSTEMS  
     327/100     SIGNAL CONVERTING, SHAPING, OR GENERATING  
     327/185     .Particular stable state circuit (e.g.,  
                   tristable, etc.)  
     327/199     ..Circuit having only two stable states (i.e.,  
                   bistable)  
     327/202     ...Master-slave bistable latch  
     327/203     ....Including field-effect transistor
- 2 327/212 (0 OR, 2 XR)  
     Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR  
                   DEVICES, CIRCUITS, AND SYSTEMS

10068898\_CLSTITLES

327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING

327/185 .Particular stable state circuit (e.g.,  
tristable, etc.)

327/199 ..Circuit having only two stable states (i.e.,  
bistable)

327/208 ...Including field-effect transistor

327/212 ....With clock input

2 327/291 (1 OR, 1 XR)  
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR  
DEVICES, CIRCUITS, AND SYSTEMS

327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING

327/291 .Clock or pulse waveform generating

2 365/189.01 (1 OR, 1 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL

365/189.01 READ/WRITE CIRCUIT

2 365/189.05 (2 OR, 0 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL

365/189.01 READ/WRITE CIRCUIT

365/189.05 .Having particular data buffer or latch

2 365/189.07 (0 OR, 2 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL

365/189.01 READ/WRITE CIRCUIT

365/189.07 .Including signal comparison

2 365/207 (1 OR, 1 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL

365/189.01 READ/WRITE CIRCUIT

365/206 .Noise suppression

365/207 ..Differential sensing

2 365/219 (2 OR, 0 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL

365/189.01 READ/WRITE CIRCUIT

365/219 .SiPo/PiSo

2 365/220 (1 OR, 1 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL

365/189.01 READ/WRITE CIRCUIT

365/220 .Parallel read/write

2 365/226 (0 OR, 2 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL

365/226 POWERING

10068898\_CLSTITLES

- 2 365/230.06 (0 OR, 2 XR)  
     Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
     365/230.01 ADDRESSING  
     365/230.06 .Particular decoder or driver circuit
- 2 365/236 (0 OR, 2 XR)  
     Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
     365/230.01 ADDRESSING  
     365/236 .Counting
- 2 365/239 (0 OR, 2 XR)  
     Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
     365/230.01 ADDRESSING  
     365/239 .Sequential
- 2 377/44 (1 OR, 1 XR)  
     Class 377 : ELECTRICAL PULSE COUNTERS, PULSE DIVIDERS, OR  
                 SHIFT REGISTERS: CIRCUITS AND SYSTEMS  
     377/27 SYSTEMS  
     377/44 .Counter controlled counter
- 2 710/53 (1 OR, 1 XR)  
     Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
                 PROCESSING SYSTEMS: INPUT/OUTPUT  
     710/1 INPUT/OUTPUT DATA PROCESSING  
     710/52 .Input/Output data buffering  
     710/53 ..Alternately filling or emptying buffers
- 2 711/157 (1 OR, 1 XR)  
     Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
                 SYSTEMS: MEMORY  
     711/100 STORAGE ACCESSING AND CONTROL  
     711/154 .Control technique  
     711/157 ..Interleaving
- 2 714/727 (1 OR, 1 XR)  
     Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
                 DETECTION/RECOVERY  
     714/699 PULSE OR DATA ERROR HANDLING  
     714/724 .Digital logic testing  
     714/726 ..Scan path testing (e.g., level sensitive sca  
     n design (LSSD))  
     714/727 ...Boundary scan
- 2 716/6 (2 OR, 0 XR)  
     Class 716 : DATA PROCESSING: DESIGN AND ANALYSIS OF  
                 CIRCUIT OR SEMICONDUCTOR MASK

		10068898_CLSTITLES
	716/1	CIRCUIT DESIGN
	716/4	.Testing or evaluating
	716/5	..Design verification (e.g., wiring line
		capacitance, fan-out checking, minimum pat
h width)	716/6	...Timing analysis (e.g., delay time, path
		delay, latch timing)



98

PLUS Search Results for S/N 10068898, Searched June 15, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

4527075  
6055587  
6188623  
5192914  
5524098  
5617367  
6005412  
6272667  
6456138  
6668363  
6237122  
5255383  
5388074  
5734285  
5633606  
6239611  
5017808  
5426380  
5493530  
5831463  
6002282  
6018559  
6026473  
4882709  
4935942  
4954992  
5321733  
5373200  
5376848  
5544104  
5566119  
5587953  
5621740  
5670896  
5768624

10068898\_LIST

5834947  
5850150  
5887040  
5886904  
5920218  
6043696  
6066961  
6078623  
6081476  
6121807  
6166973  
6208275  
6215725  
6215725  
4296477

10068898\_QUAL

4527075 99  
6055587 99  
6188623 99  
5192914 99  
5524098 99  
5617367 99  
6005412 99  
6272667 99  
6456138 99  
6668363 99  
6237122 86  
5255383 86  
5388074 86  
5734285 86  
5633606 86  
6239611 86  
5017808 86  
5426380 86  
5493530 86  
5831463 86  
6002282 86  
6018559 86  
6026473 86  
4882709 86  
4935942 86  
4954992 86  
5321733 86  
5373200 86  
5376848 86  
5544104 86  
5566119 86  
5587953 86  
5621740 86  
5670896 86  
5768624 86  
5834947 86  
5850150 86  
5887040 86  
5886904 86  
5920218 86  
6043696 86  
6066961 86  
6078623 86  
6081476 86  
6121807 86  
6166973 86  
6208275 86  
6215725 86

10068898\_QUAL

6215725 86  
4296477 86